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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/965,937	09/28/2001	Katsuya Anzai	YKI-0074	3361

23413 7590 06/01/2004

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EXAMINER	
WU, XIAO MIN	

ART UNIT	PAPER NUMBER
2674	

DATE MAILED: 06/01/2004

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Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No. 09/965,937	Applicant(s) ANZAI ET AL.	
	Examiner XIAO M. WU	Art Unit 2674	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1)☒ Responsive to communication(s) filed on *17 March 2004*.

2a)☒ This action is **FINAL**. 2b)☐ This action is non-final.

3)☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4)☒ Claim(s) *1 and 3-13* is/are pending in the application.

4a) Of the above claim(s) _____ is/are withdrawn from consideration.

5)☒ Claim(s) *1,3-9,12 and 13* is/are allowed.

6)☒ Claim(s) *10 and 11* is/are rejected.

7)☐ Claim(s) _____ is/are objected to.

8)☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

9)☐ The specification is objected to by the Examiner.

10)☐ The drawing(s) filed on _____ is/are: a)☐ accepted or b)☐ objected to by the Examiner.

Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).

11)☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

12)☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

a)☐ All b)☐ Some * c)☐ None of:

1.☐ Certified copies of the priority documents have been received.

2.☐ Certified copies of the priority documents have been received in Application No. _____.

3.☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

1)☐ Notice of References Cited (PTO-892)
2)☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3)☐ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date _____.
4)☐ Interview Summary (PTO-413) Paper No(s)/Mail Date. _____ .
5)☐ Notice of Informal Patent Application (PTO-152)
6)☐ Other: _____.

DETAILED ACTION

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

> A person shall be entitled to a patent unless –
>
> (e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

2. Claims 10 and 11 are rejected under 35 U.S.C. 102(e) as being anticipated by Dawson et al. (US Patent No. 6,229,506).

As to claim 10, Dawson discloses a semiconductor device comprising: a switching thin film transistor (P4, 240, Fig. 2) which operates by receiving a gate signal (210, Fig. 2) at its gate (G, Fig. 2) and for reading a data signal (220, Fig. 2); and an element driving thin film transistor (P2-260, Fig. 2) provided between a driving power supply (295, Fig. 2) and an element (290, Fig. 2) to be driven, for controlling the power supplied from the driving power supply to the element to be driven based on a data signal supplied from the switching thin film transistor; characterized in that a compensation thin film transistor (N1-270, Fig. 2) having an opposite conductive characteristic (e.g. N-type transistor) with respect to the element driving thin film transistor (e.g. P-type transistor 260) is provided between the driving power supply (295) and the element driving thin film transistor (260). Dawson further discloses that the element driving thin film transistor (375, Fig. 3) and the compensation thin film transistor (365, Fig. 3) are placed so that a channel length direction (the gate channel length direction) of the thin film transistors is along

the extension direction of the data line (310) for supplying the data signal to the switching thin film transistor (360, Fig. 3).

As to claim 11, Dawson discloses a semiconductor device comprising: a switching thin film transistor (P4, 240, Fig. 2) which operates by receiving a gate signal (210, Fig. 2) at its gate (G, Fig. 2) and for reading a data signal (220, Fig. 2); and an element driving thin film transistor (P2-260, Fig. 2) provided between a driving power supply (295, Fig. 2) and an element (290, Fig. 2) to be driven, for controlling the power supplied from the driving power supply to the element to be driven based on a data signal supplied from the switching thin film transistor; characterized in that a compensation thin film transistor (N1-270, Fig. 2) having an opposite conductive characteristic (e.g. N-type transistor) with respect to the element driving thin film transistor (e.g. P-type transistor 260) is provided between the driving power supply (295) and the element driving thin film transistor (260). Dawson further discloses that a channel length direction of the element driving thin film transistor does not coincide with the channel length direction of the switching thin film transistor. For example, as shown Fig. 3, the channel of the element driving TFT 375 is in a vertical direction and the channel of the switching TFT 360 is in a horizontal direction.

Allowable Subject Matter

3. Claims 1, 3-9 and 12-13 are allowed.

Response to Arguments

4. Applicant's arguments filed 3/17/2004 have been fully considered but they are not persuasive. With respect to claims 10 and 11, Applicant argues that a circuit diagram only

represents electrical connection relationships, and very often does not match with the actual structural layout, and the fact that directions indicated in a circuit.diagram do not determine the actual structural layout is common knowledge in both the semiconductor device field and the field of displays. Applicant's argument is not persuasive. For example, the US Patent No. 6,136,632 to Higashi clearly shows that the channel length direction in the diagram of the TFT is the channel length direction in the actual structural layout of the TFT (see Fig. 1A and col. 11, lines 49-50).

Conclusion

5. **THIS ACTION IS MADE FINAL.** Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Xiao Wu whose telephone number is (703) 305-4721.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Richard Hjerpe, can be reached on (703) 305-4709.

Any response to this action should be mailed to:

Commissioner of Patents and Trademarks
Washington, D.C. 20231

or faxed to:

(703) 872-9306

Hand-delivered responses should be brought to Crystal Park II, 2121 Crystal Drive, Arlington. VA., Sixth Floor (Receptionist).

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the Technology Center 2600 Customer Service Office whose telephone number is (703) 306-0377

xw
May 31, 2004

XIAO WU
PRIMARY EXAMINER
ART UNIT 2674